

## **REMARKS**

Claims 1 and 7-9 remain in the application for consideration of the Examiner.

Reconsideration and withdrawal of the outstanding objections and rejections are respectfully requested in light of the above amendments and following remarks.

Claim 9 was objected to because of informalities.

By the instant amendment, Claims 1 and 7-9 have been amended to take into consideration the helpful comments of the Examiner.

It is respectfully submitted that Claims 1 and 7-9 are free from informalities.

Claims 1 and 7-9 were rejected under the judicially created doctrine of obviousness-type double patenting.

This rejection is respectfully traversed.

Since the two applications have the same filing date, it is not possible to extend in monopoly since both should expire on the same day.

Claims 8 and 9 were rejected under 35 U.S.C. § 112, second paragraph, as being indefinite.

By the instant amendment, Claims 8 and 9 have been amended to take into consideration the helpful comments of the Examiner.

It is respectfully submitted that Claims 1 and 7-9 are in full compliance with 35 U.S.C. § 112, and particularly points out and distinctly claims the subject matter in which Applicants believe is their invention.

In light of the above, it is respectfully submitted that Claims 1 and 7-9 are in full compliance with the judicially created doctrine of obviousness-type double patenting.

Claim 1 was rejected under 35 U.S.C. § 102(b) as being anticipated by Yokosawa.

Claims 1 and 7-9 were rejected under 35 U.S.C. § 103 as being unpatentable over alleged applicants admitted prior art (AAPA) in view of Yokosawa.

These rejections are respectfully traversed.

It is respectfully submitted that AAPA does not disclose or suggest the presently claimed invention including the drive circuit including a clamp circuit.

Applicants agree with the Examiner as evidenced by the Office Action.

Furthermore, Yokosawa does not disclose or suggest the presently claimed invention including the clamp circuit to hold the drain terminal of the current output MOS transistor at a predetermined potential.

Yokosawa discloses that the clamp circuit 28 clamps the gate potential of the source follower transistor 8.

The gate and drain are different elements. Clamping the gate does not mean that the voltage at the drain cannot vary.

In light of the above, it is respectfully submitted that the present application is in condition for allowance, and notice to that effect is respectfully requested.

While it is believed that the instant response places the application in condition for allowance, should the Examiner have any further comments or suggestions, it is

respectfully requested that the Examiner contact the undersigned in order to expeditiously resolve any outstanding issues.

To the extent necessary, Applicant petitions for an Extension of Time under 37 CFR 1.136. Please charge any fees in connection with the filing of this paper, including extension of time fees, to the deposit account of Texas Instruments Incorporated, Account No. 20-0668.

Respectfully submitted,



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